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1 **Probability table compression for handwritten character recognition**
Sung-Jung Cho; Perrone, M.; Ratzlaff, E.;
Document Analysis and Recognition, 2003. Proceedings. Seventh Internationa Conference on , 3-6 Aug. 2003
Pages:173 - 177 vol.1

[Abstract] [PDF Full-Text (2022 KB)] **IEEE CNF**

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1 Memory system performance of UNIX on CC-NUMA multiprocessors
John Chapin, A. Herrod, Mendel Rosenblum, Anoop Gupta
May 1995 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1995 ACM SIGMETRICS joint international conference on Measurement and modeling of computer systems**, Volume 23 Issue 1

Full text available: pdf(1.78 MB) Additional Information: full citation, abstract, references, citings, index terms

This study characterizes the performance of a variant of UNIX SVR4 on a large shared-memory multiprocessor and analyzes the effects of possible OS and architectural changes. We use a nonintrusive cache miss monitor to trace the execution of an OS-intensive multiprogrammed workload on the Stanford DASH, a 32-CPU CC-NUMA multiprocessor (CC-NUMA multiprocessors have cache-coherent shared memory that is physically distributed across the machine). We find that our version of UNIX accounts for 24% of ...

2 Technical papers: component technologies: Component rank: relative significance rank for software component search
Katsuro Inoue, Reishi Yokomori, Hikaru Fujiwara, Tetsuo Yamamoto, Makoto Matsushita, Shinji Kusumoto
May 2003 **Proceedings of the 25th International Conference on Software Engineering**

Full text available: pdf(884.54 KB) Publisher Site Additional Information: full citation, abstract, references, citings, index terms

Collections of already developed programs are important resources for efficient development of reliable software systems. In this paper, we propose a novel method of ranking software components, called *Component Rank*, based on analyzing actual use relations among the components and propagating the significance through the use relations. We have developed a component-rank computation system, and applied it to various Java programs. The result is promising such that non-specific and generic ...

3 Unidraw: a framework for building domain-specific graphical editors
John M. Vlissides, Mark A. Linton
July 1990 **ACM Transacti ns n Inf rmati n Systems (TOIS)**, Volume 8 Issue 3

Full text available: pdf(2.52 MB) Additional Information: full citation, abstract, references, citings, index terms, review

Unidraw is a framework for creating graphical editors in domains such as technical and artistic drawing, music composition, and circuit design. The Unidraw architecture simplifies

the construction of these editors by proving programming abstractions that are common across domains. Unidraw defines four basic abstractions: components define operations on components, and external representations define the mapping between components and the file format generat ...

4 Generating spreadsheet-like tools from strong attribute grammars

João Saraiva, Doaitse Swierstra

September 2003 **Proceedings of the second international conference on Generative programming and component engineering**

Full text available: pdf(161.50 KB) Additional Information: full citation, abstract, references, index terms

This paper presents techniques for the formal specification and efficient incremental implementation of spreadsheet-like tools. The spreadsheets are specified by strong attribute grammars. In this style of attribute grammar programming every single inductive computation is expressed within the attribute grammar formalism. Well-known attribute grammar techniques are used to reason about such grammars. For example, ordered scheduling algorithms can be used to statically guarantee termination of th ...

5 Unidraw: a framework for building domain-specific

J. M. Vlissides, M. A. Linton

November 1989 **Proceedings of the 2nd annual ACM SIGGRAPH symposium on User interface software and technology**

Full text available: pdf(1.19 MB) Additional Information: full citation, abstract, references, citings, index terms

Unidraw is a framework for creating object-oriented graphical editors in domains such as technical and artistic drawing, music composition, and CAD. The Unidraw architecture simplifies the construction of these editors by providing programming abstractions that are common across domains. Unidraw defines four basic abstractions: components encapsulate the appearance and behavior of objects, tools support direct manipulation of components, commands define operations on components, and externa ...

6 Metrics for Ada packages: an initial study

J. D. Gannon, E. E. Katz, V. R. Basili

July 1986 **Communications of the ACM**, Volume 29 Issue 7

Full text available: pdf(719.26 KB) Additional Information: full citation, abstract, references, citings, index terms, review

Many novel features of Ada present programmers with a formidable learning task. The study of four first-time Ada programmers suggests that a background in the software engineering practices supported by Ada is necessary to learn to use the features of the language.

7 Design tradeoffs for the Alpha EV8 conditional branch predictor

André Seznec, Stephen Felix, Venkata Krishnan, Yiannakis Sazeides

May 2002 **ACM SIGARCH Computer Architecture News**, Volume 30 Issue 2

Full text available: pdf(1.24 MB) Publisher Site Additional Information: full citation, abstract, references, citings, index terms

This paper presents the Alpha EV8 conditional branch predictor The Alpha EV8 microprocessor project, canceled in June 2001 in a late phase of development, envisioned an aggressive 8-wide issue out-of-order superscalar microarchitecture featuring a very deep pipeline and simultaneous multithreading. Performance of such a processor is highly dependent on the accuracy of its branch predictor and consequently a very large silicon area was devoted to branch prediction on EV8. The Alpha EV8 branch pre ...

Keyw rds: EV8 processor, Branch Prediction

8 A checkpointing strategy for scalable recovery on distributed parallel systems
Vijay K. Naik, Samuel P. Midkiff, Jose E. Moreira
November 1997 **Proceedings of the 1997 ACM/IEEE conference on Supercomputing (CDROM)**
Full text available: pdf(144.90 KB) Additional Information: full citation, abstract, references

In this paper, we describe a new scheme for checkpointing parallel applications on message-passing scalable distributed memory systems. The novelty of our scheme is that a checkpointed application can be restored, from its checkpointed state, in a reconfigured form. Thus, a parallel application may be checkpointed while executing with **t1** tasks on **p1** processors, and then restarted from the checkpointed state with **t2** tasks on **p2**
Keywords: DRMS, IBM RS/6000 SP, checkpointing and restart, parallel checkpointing, reconfigurable checkpointing, scalable recovery

9 HISDL—a structure description language
Willie Y.-P. Lim
November 1982 **Communications of the ACM**, Volume 25 Issue 11
Full text available: pdf(702.72 KB) Additional Information: full citation, abstract, references, citings, index terms

The features of a language designed for the description of the structure of computer systems are described. The structure of a system is specified hierarchically as an interconnection of components with each component being a named instance of a component type. The system itself is another component type. The interconnection between components is specified in two ways: either by specifying all the ports that are connected together, or by specifying a component and the ports that are connect ...

Keywords: connection specification, hardware description language, hierarchical structure description, iterative structure description, structure description language

10 A look at several memory management units, TLB-refill mechanisms, and page table organizations
Bruce L. Jacob, Trevor N. Mudge
October 1998 **Proceedings of the eighth international conference on Architectural support for programming languages and operating systems**, Volume 32 , 33 Issue 5 , 11
Full text available: pdf(1.90 MB) Additional Information: full citation, abstract, references, citings, index terms

Virtual memory is a staple in modem systems, though there is little agreement on how its functionality is to be implemented on either the hardware or software side of the interface. The myriad of design choices and incompatible hardware mechanisms suggests potential performance problems, especially since increasing numbers of systems (even embedded systems) are using memory management. A comparative study of the implementation choices in virtual memory should therefore aid system-level designers ...

11 Cost estimation for component based software development
Randy K. Smith, Allen Parrish, Joanne Hale
April 1998 **Pr ceedings f the 36th annual Southeast regional c nference**
Full text available: pdf(442.87 KB) Additional Information: full citation, references, index terms

12 An ECL logic synthesis system
Van Morgan, David Gregory
June 1991 **Pr ceedings of the 28th c nference on ACM/IEEE design aut mati n**
Full text available: pdf(614.83 KB) Additional Information: full citation, references, citings, index terms

13 A framework for modeling and implementing visual notations with applications to software engineering
Gennaro Costagliola, Vincenzo Deufemia, Giuseppe Polese
October 2004 **ACM Transactions on Software Engineering and Methodology (TOSEM)**, Volume 13 Issue 4
Full text available: pdf(3.77 MB) Additional Information: full citation, abstract, references, index terms

We present a framework for modeling visual notations and for generating the corresponding visual programming environments. The framework can be used for modeling the diagrammatic notations of software development methodologies, and to generate visual programming environments with CASE tools functionalities. This is accomplished through an underlying modeling process based on the visual notation syntactic model of eXtended Positional Grammars (XPG, for short), and the associated parsing methodolo ...

Keywords: LR parsing, UML, meta-CASE, metamodeling, software engineering models, visual grammars, visual notations

14 Exploiting weak connectivity for mobile file access
L. B. Mummert, M. R. Ebling, M. Satyanarayanan
December 1995 **ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles**, Volume 29 Issue 5
Full text available: pdf(1.49 MB) Additional Information: full citation, references, citings, index terms

15 Hive: fault containment for shared-memory multiprocessors
J. Chapin, M. Rosenblum, S. Devine, T. Lahiri, D. Teodosiu, A. Gupta
December 1995 **ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles**, Volume 29 Issue 5
Full text available: pdf(1.90 MB) Additional Information: full citation, references, citings, index terms

16 Learning to recognize tables in free text
Hwee Tou Ng, Chung Yong Lim, Jessica Li Teng Koo
June 1999 **Proceedings of the 37th conference on Association for Computational Linguistics**
Full text available: pdf(736.27 KB) Additional Information: full citation, abstract, references

Many real-world texts contain tables. In order to process these texts correctly and extract the information contained within the tables, it is important to identify the presence and structure of tables. In this paper, we present a new approach that learns to recognize tables in free text, including the boundary, rows and columns of tables. When tested on Wall Street Journal news documents, our learning approach outperforms a deterministic table recognition algorithm that identifies table reco ...

17 Manufacturing applications: Distributed simulation in manufacturing: automobile

manufacturing supply chain simulation in the grids environment
Gary Tan, Na Zhao, Simon J. E. Taylor
December 2003 **Pr ceedings f the 35th conference n Winter simulation: driving innovation**
Full text available: pdf(522.61 KB) Additional Information: full citation, abstract, references

A Supply Chain is the series of activities that an organization uses to deliver value to its customers. In today's competitive environment, the globalization of markets has rapidly substituted the traditional integrated business. The competitive success of an organization no longer depends only on its own efforts, but relies on the efficiency of the entire supply chain. Therefore, building an effective supply chain is fast becoming paramount in today's marketplace. Distributed Supply Chain (D ...

18 Simulation-based scheduling: Maintenance and repair: a comparison of three optimization methods for scheduling maintenance of high cost, long-lived capital assets
Terry M. Helm, Steve W. Painter, W. Robert Oakes
December 2002 **Proceedings of the 34th conference on Winter simulation: exploring new frontiers**
Full text available: pdf(320.56 KB) Additional Information: full citation, abstract, references

A range of minimization methods exist enabling planners to tackle tough scheduling problems. We compare three scheduling techniques representative of "old" or standard technologies, evolving technologies, and advanced technologies. The problem we address includes the complications of scheduling long-term upgrades and refurbishments essential to maintaining expensive capital assets. We concentrate on the costs of being able to do maintenance work. Using a standard technology as the baseline te ...

19 Manufacturing applications: Manufacturing modeling methods: virtual reality simulation of a mechanical assembly production line
Deogratias Kibira, Chuck McLean
December 2002 **Proceedings of the 34th conference on Winter simulation: exploring new frontiers**
Full text available: pdf(416.71 KB) Additional Information: full citation, abstract, references

This paper presents our work on the application of virtual-reality simulation to the design of a production line for a mechanically-assembled product. The development of this simulation was undertaken as a part of the Manufacturing Simulation and Visualization Program at the National Institute of Standards and Technology in Gaithersburg, MD. The major research problem is the partitioning and analysis of the assembly operation of the prototype product into different tasks and allocation of the ...

20 Technical papers: software maintenance: Evolving legacy system features into fine-grained components
Alok Mehta, George T. Heineman
May 2002 **Proceedings of the 24th International Conference on Software Engineering**
Full text available: pdf(1.42 MB) Additional Information: full citation, abstract, references, index terms

There is a constant need for practical, efficient, and cost-effective software evolution techniques. We propose a novel evolution methodology that integrates the concepts of features, regression tests, and component-based software engineering (CBSE). Regression test cases are untapped resources, full of information about system features. By exercising each feature with its associated test cases using code profilers and similar tools, code can be located and refactored to create components. These ...

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L62	0	L61 and @ad>"20040717"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 13:34
L66	0	L65 and @ad>"20040717"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 13:34
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L75	0	46 and @ad>"20040717"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 13:42
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L78	47	L72 and @ad<"19981222"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 13:45

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L3	226	706/11.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 12:28
L4	224	706/14.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 12:28
L5	91	706/2.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 12:28
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L7	143	706/59.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 12:28
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L10	865	1 xor 2 1 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 12:29

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L13	377	7 xor 8 7 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 12:29
L14	928	9 xor 10 9 and 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 12:29
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L41	30	34 and @ad<="19981222"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/14 12:44
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